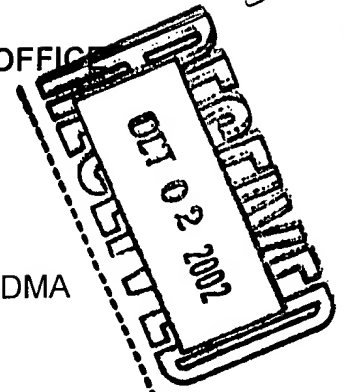




IN THE UNITED STATES PATENT AND TRADEMARK OFFICE



26: #4
10-030

Applicants: Stephen E. J. Blightman, et al.

Assignee: Alacritech, Inc.

Title: "NETWORK INTERFACE DEVICE EMPLOYING A DMA
COMMAND QUEUE"

Serial No.: 09/855,979

Filed: May 14, 2001

RECEIVED

Examiner:

Tel:

OCT 01 2002

Docket No.: ALA-016

Art Unit: 2663

Technology Center 2600

Box DD
Commissioner For Patents
Washington, D.C. 20231

September 25, 2002

INFORMATION DISCLOSURE STATEMENT

Sir:

Pursuant to 37 C.F.R. §1.56, §1.97 and §1.98, Applicants bring the 61 documents listed on the enclosed Form PTO-1449 to the Examiner's attention in the above-captioned application. Citation of the listed documents shall not be construed as:

1. an admission that the documents are necessarily prior art with respect to the instant application;
2. a representation that a search has been made; or
3. an admission that the information cited is, or is considered to be, material to patentability as defined in §1.56(b).

A copy of each of the listed documents is submitted.

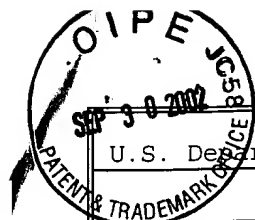
I hereby certify that this correspondence is being deposited with the United States Postal Service as "First Class Mail" on the date indicated below and is addressed to: Box DD, Commissioner for Patents, Washington, D.C. 20231.

Sept. 25, 2002 T. Lester Wallace
Date T. Lester Wallace

Respectfully submitted,

T. Lester Wallace

T. Lester Wallace
Attorney for Applicants
Reg. No. 34,748



U.S. Department of Commerce, Patent and Trademark Office	Application No.: 09/855,979
	Filing date: May 14, 2001
INFORMATION DISCLOSURE STATEMENT BY APPLICANT	Inventors: Blightman, et al.
	Group Art Unit: 2663
"NETWORK INTERFACE DEVICE EMPLOYING A DMA COMMAND QUEUE"	Examiner name: Unknown
	Attorney Docket No. ALA-016

U.S. Patent Documents

*Examiner Initial		Document Number	Date	Name	Class	Subclass	Filing Date, If Appropriate
	AA	5,701,516	12/23/97	Cheng et al.	395	842	
	AB	5,809,328	09/15/98	Nogales et al.	395	825	
	AC	5,931,918	08/03/99	Row et al.	709	300	
	AD	5,935,205	08/10/99	Murayama et al.	709	216	
	AE	5,937,169	08/10/99	Connery et al.	395	200.8	
	AF	5,941,969	08/24/99	Ram et al.	710	128	
	AG	5,941,972	08/24/99	Hoese et al.	710	129	
	AH	5,950,203	09/07/99	Stakuis et al.	707	10	
	AI	5,996,024	11/30/99	Blumenau	709	301	
	AK	6,005,849	12/21/99	Roach et al.	370	276	
	AL	6,016,513	01/18/00	Glen H. Lowe	709	250	
	AM	6,026,452	02/15/00	William Michael Pitts	710	56	
	AN	6,044,438	03/28/00	Howard Thomas Olnowich	711	130	
	AO	6,047,356	04/04/00	Anderson et al.	711	129	
	AP	6,057,863	05/02/00	Sompong P. Olarig	345	520	
	AQ	6,065,096	05/16/00	Day et al.	711	114	
	AR	6,141,705	10/31/00	Anand et al.	710	15	
	AS	6,246,683	06/12/01	Connery et al.	370	392	
	AT	6,226,680	05/01/01	Boucher et al.	709	230	
	AU	6,247,060	06/12/01	Boucher et al.	709	238	
	AV	08/964,304		Napolitano et al.			

RECEIVED

OCT 11 2002

Technology Center 2600

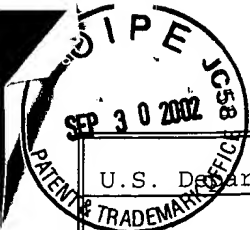
Foreign Patent Documents

							Translation	
		Document Number	Date ,	Country	Class	Subclass	Yes	No

OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)

Examiner	Date Considered
----------	-----------------

*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with your communication to applicant.



U.S. Department of Commerce, Patent and Trademark Office	Application No.: 09/855,979
	Filing date: May 14, 2001
INFORMATION DISCLOSURE STATEMENT BY APPLICANT	Inventors: Blightman, et al.
	Group Art Unit: 2663
"NETWORK INTERFACE DEVICE EMPLOYING A DMA COMMAND QUEUE"	Examiner name: Unknown
	Attorney Docket No. ALA-016

Foreign Patent Documents

							Translation	
		Document Number	Date	Country	Class	Subclass	Yes	No
	BA	WO 99/65219 (PCTUS99/13184)	12/16/99	International				
	BB	WO 01/05107 A1 (PCT US00/19006)	01/18/01	International				
	BC	WO 01/05116 A2 (PCT US00/19243)	01/18/01	International				
	BD	WO 01/05123 A1 (PCT US00/18976)	01/18/01	International				
	BE	WO 01/40960 A1 (PCT US00/32660)	06/07/01	International				
	BF	WO 01/04770 A2 (PCT US00/18939)	1/18/01	International				

RECEIVED

OCT 01 2002

Technology Center 2600

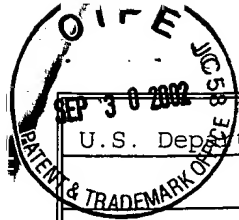
OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)

BG	Web pages entitled "iReady Rounding Out Management Team With Two Key Executives", 2 pages (printed 11/28/98).
BH	Toshiba, "Toshiba Delivers First Chips To Make Consumer Devices Internet-Ready Based On iReady 's Design," Press Release 10/14/98, 3 pages.
BI	Web pages, www.ireadyco.com, iReady, "Products," 2 pages. Printed 11/25/98.
BJ	Info Work, "Toshiba , iReady shipping internet chip," by Bob Gruth, 1 page , printed 11/28/98.
BK	Internet site www.interprophet.com , 17 pages, printed 3/1/00.
BL	The I-1000 Internet Tuner, iReady Corporation, 2 pages date unknown.
BM	Web pages from www.ireadyco.com , "Introduction," 3 pages, downloaded 11/25/98.
BN	IReady New Archive, "Revolutionary Approach to Consumer Electronics Internet Connectivity Funded ", San Jose, CA, 2 pages, 11/20/97.
BO	IReady News Archive, "Seiko Instrument s Inc. (SII) Introduces World's First Internet-Ready Intelligent LCD Modules Based on IReady Technology, by Endo, Brogan and Johnson, printed 11/2/98.
BP	NEWSwatch -iReady Internet Tuner to Web Enable Devices, November 5, 1996, 2 pages, printed 11/2/98.

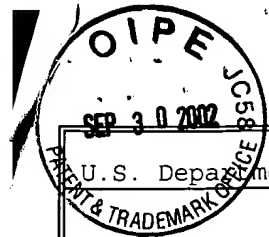
Examiner

Date Considered

*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with your communication to applicant.



U.S. Department of Commerce, Patent and Trademark Office		Application No.: 09/855,979
		Filing date: May 14, 2001
INFORMATION DISCLOSURE STATEMENT BY APPLICANT		Inventors: Blightman, et al.
		Group Art Unit: 2663
"NETWORK INTERFACE DEVICE EMPLOYING A DMA COMMAND QUEUE"		Examiner name: Unknown
		Attorney Docket No. ALA-016
OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)		
DA	David Lammers, EETimes, 06/13/97, Tuner for Toshiba, Toshiba Taps iReady For Internet Tuner, 3 pages, printed 11/02/98.	
DB	Internet pages entitled: "Comparison of Novell Netware and TCP/IP Protocol Architectures," by Janique Carbone, July 16 1995, 19 pages 4/10/98.	
DC	Internet web pages from "Adaptec.com" website directed to the Adaptec, AEA-7110C iSCSI Host Bus Adapter... 11 pages, downloaded 10/1/01.	
DD	iSCSI HBA article entitled "FCE-3210/6410 32 and 64-bit PCI-to-Fibre Channed HBA", 6 pages, printed 10/01/01.	
DE	Internet web pages from the "iSCSI Storage .com" website that mention an Emulex HBA, 2 pages downloaded 10/01/01.	
DF	Internet web pages from the "iSCSI HBA.com" website that mention QLogic HBAs including the "SANblade 2300 Series", 8 pages download 10/1/01.	
DG	"Two-Way TCP Traffic over Rate Controlled Channels: Effects and Analysis," by Lampros Kalampoukas, Anujan Varma and K.K. Ramakrishnan, IEEE Transactions on Networking, Volume 6, Number 6, 17 pages, December 1998.	
DH	iReady News Archive, "Toshiba Delivers First Chips to Make Consumer Devices Internet-Ready Based on iReady Design," 3 pages, downloaded November 2, 1998.	
DI	"File System Design for an NFS File Server Appliance," by Dave Hitz, et al., 13 pages, copyright 1996.	
DJ	Adaptec press release, "Adaptec Announces EtherStorage Technology", 2 pages May 4, 2000, printed 6/14/00.	
DK	Adaptec article entitled, "EtherStorage Frequently Asked Questions," 5 pages, downloaded July 19, 2000.	
DL	Adaptec article entitled, "EtherStorage White Paper," 7 pages, downloaded July 19, 2000.	
DM	Article entitled, "Computers; Storage," by James Berlino and Ananda Baruah, CIBC World Markets Equity Research, 9 pages, August 7, 2000.	
DN	Merrill Lynch article entitled, "Storage Futures," by Steven Milunovich, 22 pages, May 10, 2000.	
DO	CBS Market Watch article entitled "Montreal start-up battles data storage bottleneck," by Susan Taylor, 2 pages, dated March 5, 2000, 2 pages printed 3/7/00.	
DP	Internet-Draft article entitled "SCSI/TCP (SCSI over TCP)," by Satran et al., 38 pages, dated February 2000, printed 5/19/00.	
Examiner		Date Considered
<p>*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with your communication to applicant.</p>		



U.S. Department of Commerce, Patent and Trademark Office	Application No.: 09/855,979
	Filing date: May 14, 2001
INFORMATION DISCLOSURE STATEMENT BY APPLICANT	Inventors: Blightman, et al.
	Group Art Unit: 2663
"NETWORK INTERFACE DEVICE EMPLOYING A DMA COMMAND QUEUE"	Examiner name: Unknown
	Attorney Docket No. ALA-016

Foreign Patent documents

		Document Number	Date	Country	Class	Subclass	

OTHER ART (Including Author, title, Date, Pertinent pages, Etc.)

	EA	Form 10-K for Exelan, Inc., for the fiscal year ending December 31, 1987 (10 Pages).
	EB	Form 10-K for Exelan, Inc., for the fiscal year December 31, 1988 (10 pages).
	EC	"Second Supplemental Information Disclosure Statement per 37 C.F.R. §1.97(i)", dated July 29, 2002 relating to Exelan Inc. as submitted in Application Serial No. 09/464,283
	ED	WindRiver article entitled "Tornado: For Intelligent Network Acceleration", copyright 2001, 2 pages.
	EE	WindRiver White Paper entitled "Complete TCP/IP Offload for High-Speed Ethernet Networks", Copyright 2002, 7 pages.
	EF	Intel article entitled "Solving Server Bottlenecks with Intel Server Adapters", Intel Corporation, Copyright 1999, 8 pages.

Examiner

Date Considered

*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with your communication to applicant.

RECEIVED

OCT 01 2002

Technology Center 2600